

A walk in interview will be held on 16'th November 2017 at 12 noon for the position of Project Engineer in the CBM-MUCH research project of the University of Calcutta at the Department of Physics, University of Calcutta. The position is purely a temporary one and will be co-terminus with the project.

Fellowship : As per rules.

Qualifications : B.Tech (First Class or equivalent) in Computer Science or Electronics or Information Technology. Desirable experience in FPGA tools and related designs.

No TA/DA will be paid for attending the interview.