



## **UNIVERSITY OF CALCUTTA**

A walk-in-interview will be held for the sanction of one Junior Research Fellow (JRF) on purely temporary basis for the DST funded research project “**Device Design and Optimization of Complementary Tunnel FETs for Low Power Applications and Their Variability Study**” under the supervision of Prof. Abhijit Mallik, Department of Electronic Science, C.U.

**Date of Interview:** 03<sup>rd</sup> September, 2015 at 1:00 PM

**Venue:** Office of the Head, Department of Electronic Science, University of Calcutta, 92, A. P. C. Road, Kolkata – 700009.

**Essential Qualification:** M.Tech. in Electronics And Communication Engineering or equivalent. B.Tech. / M.Sc. in Electronics with GATE / NET qualification may also be considered eligible.

**Desirable:** A good background in CMOS Devices and Technology.

**Tenure:** One year or September 30, 2016, whichever is earlier.

**Emoluments:** T.E. Rs. 32,500/- p.m.

Interested candidates may appear in the interview with two sets of application furnishing CV on plain paper with attested copies of testimonials (originals to be produced for verification).

No TA/DA is admissible for appearing at the interview.

**SECRETARY**  
**UCSTA, C.U.**